

Summer Semester 2016/2017

Group plan 4TCS1

	08:15 09:00	09:15 10:00	10:15 11:00	11:15 12:00	12:15 13:00	13:15 14:00	14:15 15:00	15:15 16:00	16:15 17:00	17:15 18:00	18:15 19:00	19:15 20:00
Monday	lecture Microelectronics dr inż. Grzegorz Jabłoński <i>Department of Microelectronics and Computer Science, Building B18, room A2</i>				laboratory Computer Architecture <i>start in the 8th week of the semester, Building B18, lab. B</i>		language course język obcy 4.1		lecture Signal Processing 1 dr hab. inż. Sławomir Hausman <i>Institute of Electronics, Building B9, room 320a</i>			
Tuesday	tutorials Electronic Circuits I <i>start on 7.03 (1st part of the semester) Institute of Electronics, Building B9, room 413</i>		language course język obcy 4.2		contact hour <i>with the student government</i>		lecture Computer Architecture dr inż. Witold Marańda <i>Department of Microelectronics and Computer Science, Building B18, room A2</i>		lecture Electronic Circuits I prof. dr hab. inż. Andrzej Materka <i>Institute of Electronics, Building B9, room 413</i>			
Wed	project Team Project <i>consultation hours</i>											
Thursday				tutorials Computer Architecture <i>Building B18, room A3</i>	laboratory Team Project mgr Adrianna Kozłowska 110		language course język obcy 4.1		tutorials Wychowanie fizyczne			
Friday	laboratory Electronic Circuits I <i>Institute of Electronics, Building B9, room 215</i>		language course język obcy 4.2		laboratory Signal Processing 1 <i>Building B9, room 413</i>		laboratory Microelectronics <i>start on 3.03 Department of Microelectronics and Computer Science, Building B18</i>					