

Summer Semester 2016/2017

Group plan 4TCS2

	08:15 09:00	09:15 10:00	10:15 11:00	11:15 12:00	12:15 13:00	13:15 14:00	14:15 15:00	15:15 16:00	16:15 17:00	17:15 18:00	18:15 19:00	19:15 20:00
Monday	lecture Microelectronics dr inż. Grzegorz Jabłoński Department of Microelectronics and Computer Science, Building B18, room A2		laboratory Electronic Circuits I Institute of Electronics, Building B9, room 215		laboratory Computer Architecture start in the 8th week of the semester, Building B18, lab. B		language course język obcy 4.1		lecture Signal Processing 1 dr hab. inż. Sławomir Hausman Institute of Electronics, Building B9, room 320a			
Tuesday	tutorials Electronic Circuits I start on 7.03 (1st part of the semester) Institute of Electronics, Building B9, room 413		language course język obcy 4.2		contact hour with the student government	lecture Computer Architecture dr inż. Witold Marańda Department of Microelectronics and Computer Science, Building B18, room A2		lecture Electronic Circuits I prof. dr hab. inż. Andrzej Materka Institute of Electronics, Building B9, room 413				
Wed	project Team Project consultation hours											
Thursday				tutorials Computer Architecture Building B18, room A3	laboratory Team Project mgr Adrianna Kozłowska 110		language course język obcy 4.1		tutorials Wychowanie fizyczne			
Friday			language course język obcy 4.2		laboratory Microelectronics start on 3.03 Department of Microelectronics and Computer Science, Building B18, lab. C		laboratory Signal Processing 1 Institute of Electronics, Building B9, room 413					